

Publication number: JP11203051

Publication date: 1999-07-30

Inventor: MIZUFUJI TARO

Applicant: SONY CORP

Classification:

- international: ***G06F13/28; G06F3/06; H04N5/765; H04N5/781; H04N5/907; H04N5/92; G06F13/20; G06F3/06; H04N5/765; H04N5/781; H04N5/907; H04N5/92; (IPC1-7): G06F3/06; G06F13/28; H04N5/765; H04N5/781; H04N5/907; H04N5/92***

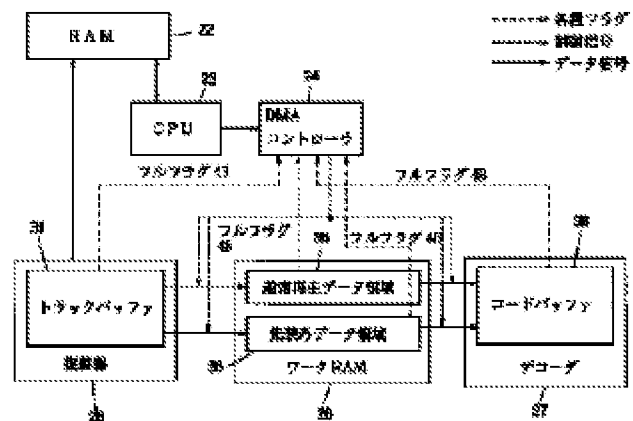
- European:

Application number: JP19980007090 19980116

Priority number(s): JP19980007090 19980116

[Report a data error here](#)

PROBLEM TO BE SOLVED: To supply data so as to instantaneously reproduce and output data corresponding to instructions for jump, variable speed reproduction, etc., while the data are reproduced. **SOLUTION:** A DMA controller 24 controls to secure a state where sufficient data are always stored in a normal reproduction data area 35 and a code buffer 38. Then the data read out of a disk are stored in a pre-read data area 36 when the sufficient data are stored. Data on an area where a jump is estimated and data necessary for variable speed reproduction, etc., are stored in the area 36.



5/2/2007

